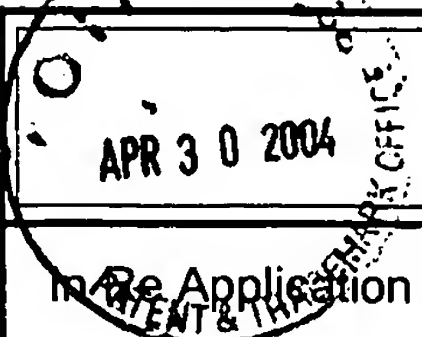
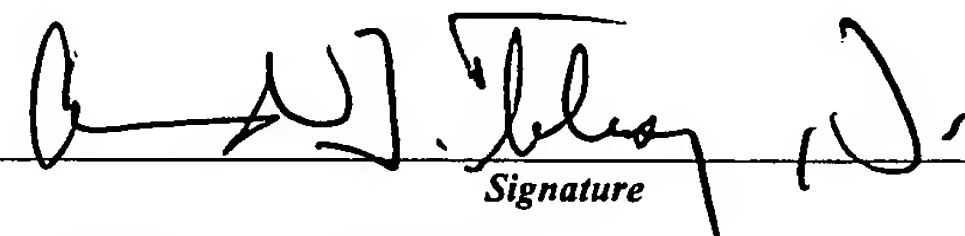
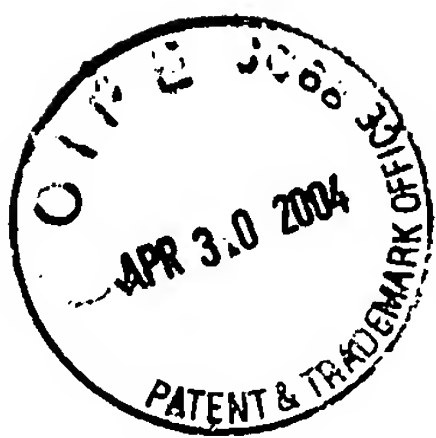


2814

<div></div> <div>TRANSMITTAL LETTER (General - Patent Pending)</div>		Docket No. KKH.039D2	
Re Application Of: Shinji Ohuchi et al.			
Serial No. 10/657,139	Filing Date September 9, 2003	Examiner D. Nguyen	Group Art Unit 2814
Title: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE INCLUDING SEMICONDUCTOR ELEMENTS MOUNTED ON BASE PLATE			
<p style="text-align: center;"><u>TO THE COMMISSIONER FOR PATENTS:</u></p> <p>Transmitted herewith is:</p> <p><b>Response to Restriction Requirement</b></p> <p>in the above identified application.</p> <p><input checked="" type="checkbox"/> No additional fee is required.</p> <p><input type="checkbox"/> A check in the amount of _____ is attached.</p> <p><input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. <b>50-0238</b> as described below.</p> <p><input type="checkbox"/> Charge the amount of _____</p> <p><input checked="" type="checkbox"/> Credit any overpayment.</p> <p><input checked="" type="checkbox"/> Charge any additional fee required.</p> <div> _____ Signature</div> <p>Dated: April 30, 2004</p> <p><b>ANDREW J. TELESZ, JR.</b> <b>REG. NO. 33,581</b></p> <p><b>VOLENTINE FRANCOS, P.L.L.C.</b> <b>12200 SUNRISE VALLEY DRIVE, SUITE 150</b> <b>RESTON, VA 20191</b></p> <p><b>TEL. NO. (703) 715-0870</b></p> <div><div>I certify that this document and fee is being deposited on _____ with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.</div><div>Signature of Person Mailing Correspondence</div><div>Typed or Printed Name of Person Mailing Correspondence</div></div> <p>CC:</p>			



KKH.039D2

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re PATENT APPLICATION of

Shinji Ohuchi et al.

Group Art Unit: 2814

Serial No.: 10/657,139

Examiner: D. Nguyen

Filed: September 9, 2003

Conf. No: 1910

FOR: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING  
SEMICONDUCTOR DEVICE INCLUDING SEMICONDUCTOR ELEMENTS  
MOUNTED ON BASE PLATE

**RESPONSE TO RESTRICTION REQUIREMENT**

U.S. Patent and Trademark Office  
2011 South Clark Place  
Crystal Plaza Two, Lobby, Room 1B03  
Arlington, VA 22202

Date: April 30, 2004

Sir:

In response to the Restriction Requirement dated April 21, 2004, the following election and remarks are respectfully submitted in connection with the above identified application.

**Remarks/Arguments** begin on page 2 of this paper.